PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Haruo NISHIDA et al.

Application No.: New US Application

Filed: January 29, 2004 Docket No.: 118496

For: TEST CIRCUIT, INTEGRATED CIRCUIT, AND TEST METHOD

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- 2. Relevance of the non-English language reference is discussed in the present specification.
- 3. An English-language Abstract of the non-English language reference is attached hereto.
- 4. A computer-generated English translation of the following Japanese Patent Publication has been obtained from the website of the Japanese Patent Office ([http://www.jpo.go.jp]), and is attached, but has not been reviewed for accuracy. See Reference 2.

Respectfully, submitted,

James A! Oliff

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JAO:JSA/tmw

Date: January 29, 2004

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	2	JP A 2001-183424 (with abstract and translation)	07/	06/2001	Japan			
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Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

DATE CONSIDERED

Date: January 29, 2004

EXAMINER

Examiner: